

ABSTRACT

A synchronous rectification circuit with dead time regulation being adapted for a forward power supply to regulate a dead time of a switch circuit is described. The synchronous rectification circuit has a first switch control
5 circuit connected with a first switch to control ON/OFF of the first switch, a dead time regulation circuit connected with the first switch control circuit to produce a dead time regulation signal, and a second switch control circuit connected with a second switch and the dead time regulation circuit to control ON/OFF of the second switch.

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